

An RTOS Based Architecture for Health Monitoring with Wireless Sensor Network

Dr.D.Satish Kumar

Abstract— RTOS architecture is designed for the purpose of data transmission between two controlling units through Wireless Sensor Networks without Collision. Here RTOS is used for to assign the priority values obtained from the patient have to be transmitted to the control room. These values are sent in the form of packets during specific time; in this transmission collision may occurred to overcome this Wireless HART (Highway Addressable Remote Transducer) is used. This project is implemented in two sections as prototype. First section runs with RTOS and LPC2148 as master node and another as normal data acquisition node to check which sensors are connected. Second section contained a controller. Communications between two nodes are accomplished through wireless HART. The basic views of this technique are to reduce the possibility of collision and thus they increase the communication reliability. The sensing parameters can be displayed as graph in software implementation. To do this, all the nodes must be synchronized precisely. Also, the stack designer must guarantee that the node can finish everything within the time slot. It offers benefits in terms of product life cycle, safety and security, system integration complexity, performance scalability.

Keywords— RTOS, μ C/OS-II, wireless HART, computing matches..

I. INTRODUCTION

A wireless sensor network consists of a large number of sensor nodes. Each sensor node has sensing, computing, and wireless communications capability [1]. Wireless process control has been a popular topic recently in the field of Health monitoring. Compared to traditional wired process control systems, their wireless counterparts have the potential to save costs and make installation easier. The majority of localization methods presuppose that some of network nodes (beacons) know their position, and these nodes act as a source for localization of the rest network nodes. Vehicles, equipped with Global Positioning Systems (GPS) receivers, are mostly used as beacon. Also, wireless technologies open up the potential for new automation applications. Several industrial organizations, such as ISA, HART, WINA and ZigBee, have been actively pushing the application of wireless technologies in health monitoring. Before Wireless health monitoring is released, there have been a few publicly available standards on office and manufacturing automation, such as ZigBee and Bluetooth [2]. However, these technologies cannot meet the stringent requirements of industrial control. Compared with

office applications, industrial applications have stricter timing requirement and higher security concern. For example, many monitoring applications are expected to retrieve updates from sensors every one second. Neither ZigBee nor Bluetooth makes any effort to provide a guarantee on end-to-end wireless communication delay. In addition, health environments are harsher for wireless applications in terms of interferences and obstacles than office environment. Some interference may be persistent [3]. ZigBee, without built-in channel hopping technique, would surely fail in such environments. Bluetooth assumes quasi-static star network, which is not scalable enough to be used in large process control systems. The new RTOS based health monitoring is specifically targeted to solve these problems and provide a complete solution for process control applications. In this paper we discuss how we developed a prototype Wireless Sensor Network protocol stack [4].

The Operating Systems (OS) can be broadly divided into two classes (i) General Purpose Operating Systems (GPOS) and (ii) Real Time Operating Systems (RTOS). Members of the GPOS class use a 'fair scheduling' algorithm to ensure that all tasks get a fair share of execution time; they do not take into account the priority of the task being executed. This is done to provide high system throughput, but can result in higher priority tasks getting delayed waiting for lower priority tasks to finish [5-7]. The RTOS class, on the other hand, uses a priority based scheduling algorithm, where higher priority tasks are preemptively scheduled ahead of lower priority tasks, ensuring higher priority tasks are not starved of CPU time.

Not generating alarms in a timely manner can have catastrophic results for a patient. This makes the real-time performance of the local node a critical design parameter.

RTOS is a Process which is done between hardware and application. Each industry has their own priority among the various tasks according to their process [8]. RTOS is used here to assign the priorities. Application code designed on an RTOS can be quite diverse, ranging from a simple application for a digital stopwatch to a much more complex application for aircraft navigation. Here Micro C/OS II is used to assign the priorities. μ C/OS-II is a real-time pre-emptive multitasking embedded OS kernel. A pre-emptive kernel is used when system responsiveness is important; therefore, μ C/OS-II and most commercial real time kernels are preemptive. μ C/OS-II is a completely portable, ROM able, scalable, real-time kernel. It provides services like task management, memory management and time management. μ C/OS-II kernel supports

pre-emptive algorithms such as round-robin, rate monotonic scheduling policy.

II. OVERVIEW ALGORITHM

Figure 1 shows high-level overview of the system. Operation can be decomposed into [9] three phases. Data sampling: sample the sensors data of the structure, and logs it into processor memory. Data collection: transfers data reliably to an external computing resource. Data Analysis: runs analysis algorithm, and determines health status. Sends feedback to nodes if needed.

The system can also be decomposed into its structural pieces. This paper is structured according to the components of the system. The system can be decomposed into three pieces: hardware, software on the node, and analysis software on a central computer. Hardware issues including sensors and power will be presented in proposed design.



Fig.1. Overview of the system

Real-time patient monitoring conditions like heart failure, temperature exceeded are generated using sensors. Such sensors to be processed in real-time and requires significant signal processing with hard real-time limits [10]. The real-time performance requirement of the monitoring system is a critical design parameter.

III. PROPOSED DESIGN ANALYSE

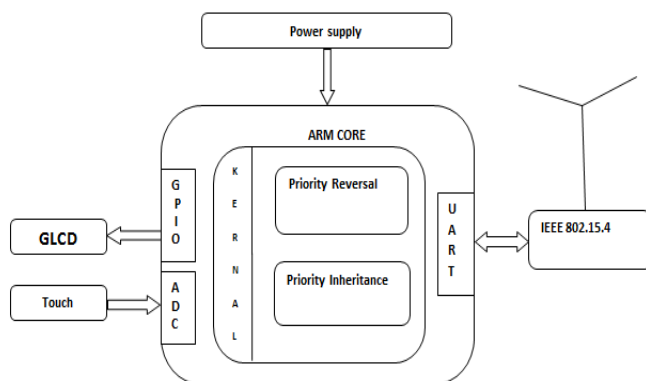


Fig. 2. Master node

Fig 2 shows the block diagram of the implemented master node system. It utilizes an RTOS running on a ARM controller that is connected to a IEEE 802.15.4 network. Data received by the network is analyzed and forwarded through the

controller to the GLCD, which displays the data in a browser window. Fig 3 shows the implemented data acquisition node of the system

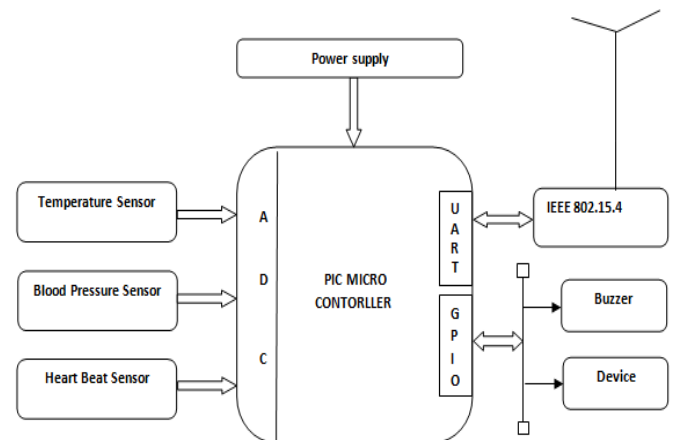


Fig. 3. Data acquisition node

A. Sensors

Some sensors used in the system may send data simultaneously at rates ranging from 1 Hz to greater than 1 KHz. The aim here was to simulate high bit rate multi-sensor data being collected from an sensor array, e.g. multi-lead Temperature and heart beat and blood presser sensor etc. To measure the multi-sensor system performance five data streams were simulated, with each data stream transmitting a signal at 1 KHz. The five data streams may represent a combination of a 3-lead signal with two other data streams coming from other sensors or from other activity on the device. This simulated sensor data is then passed through a IEEE 802.15.4 device.

B. Master Node

Patient monitoring systems are designed to generate in the case of an emergency must be able to guarantee that they can do so immediately upon discovering a problem. GPOS are not up to this task, especially when under heavy processing load (as is often the case with devices such as personal computers, tablets and smart phones). Additionally, devices used for purposes other than remote monitoring activities run the risk of potentially significant downtime should some other process crashes the system. μ C/OS-II operating system is a real-time operating system that has been used for a variety of time-critical applications, and has been designed to meet extremely strict real-time requirements [11]. The local node was implemented using the μ C/OS-II. The hardware are used for running the operating system. It utilizes an ARM 7, with 512MB of flash memory and a USD card reader that offers flexibility in short to medium-term data storage. In addition, it has built-in wireless an attached LCD touch screen display.

C. Data acquisition node

The data acquisition node designed using PIC 16F877A controller. Here all the sensors are connected ADC port of PIC controller.

This measured data's are transmitted using IEEE 802.15.4 network, and its enable the user module to watch the received data in real-time. In RTOS all the task execution time evaluation is important. For example given task 'Ti' in OS is defined by:

- Arrival time a_i when task is on ready queue.
- The worst case execution time (WCET) C_i is the maximum time taken by system to complete given task.
- Start time s_i is time when OS starts execution of the task.
- Finishing time f_i is time when OS completes the task.
- D_i is hard real-time constraint.
- $d_i = a_i + D_i$

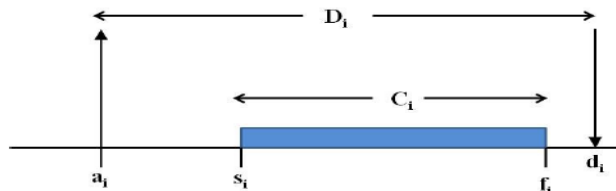


Fig.4. Parameters of Real time Task

Fig. 4 shows the parameters of the real-time tasks. The time D_i defines the time limit by which the task 'Ti' must be completed. This is the hard real-time limit. The execution time C_i is affected by other tasks/threads running on the system and must be managed by OS to ensure that $C_i < D_i$.

Latency of the system is defined as $= S_i - D_i$

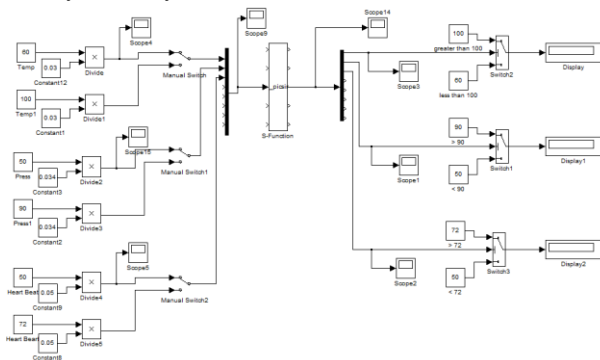


Fig.5. Full System Design

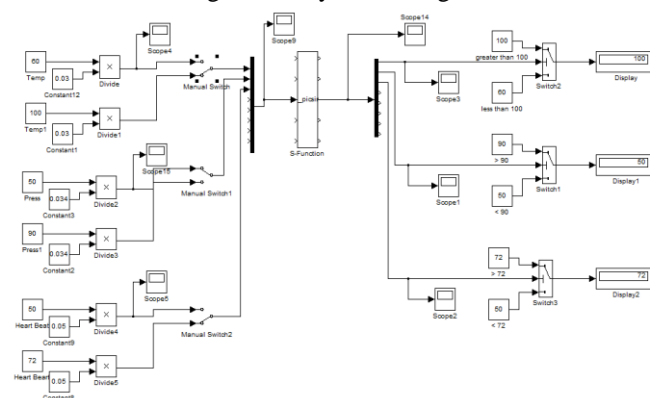


Fig.5.1. Temperature sensor o/p at 100

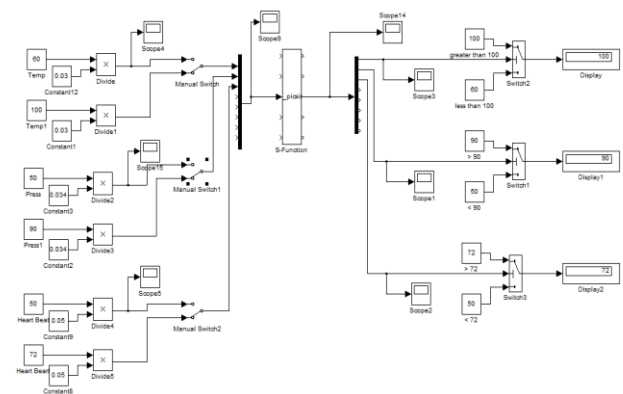


Fig.5.2. Pressure sensor o/p at 90

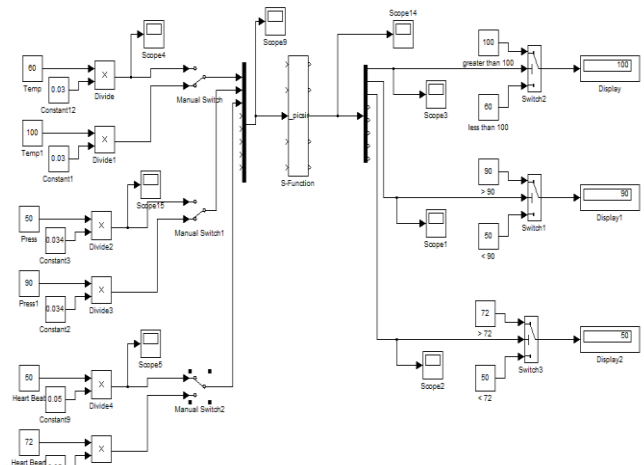


Fig. 5.3. Heart beat sensor o/p at 50

Both the latency and execution time have significant impact if the os is to meet its hard real time requirements.

Data processing algorithms for most of the sensors use windowing for performing variety of tasks like filtering, spatial and frequency domain processing, segmentation, energy calculation, threshold, etc. All these tasks must be executed within timeframe for each window. This puts a hard real-time performance requirement on the mobile device. The execution time window for a given task decreases as sampling rate increases.

IV. EXPERIMENTAL RESULTS

A. Simulation Result

In this Simulation three sensors will be connected to the Analog to Digital Converter. This will used to read the values from sensor. After Reading values it will be given to the PIC microcontroller. Microcontroller will accept only a 5 voltage so the sensor readings are converted as below 5V.

At end of the display unit the sensor values again converted into the normal values, then it will be displayed. The Displayed value depend on the given switch condition in the Switching.

The below figure 5.1 shows the design of the overall system. By changing the switching condition it can vary the Output of the Display unit. The switching operation depends

on the Low - High combination of each sensor. In this design one low value and one high value for each sensor.

B. Hardware Result

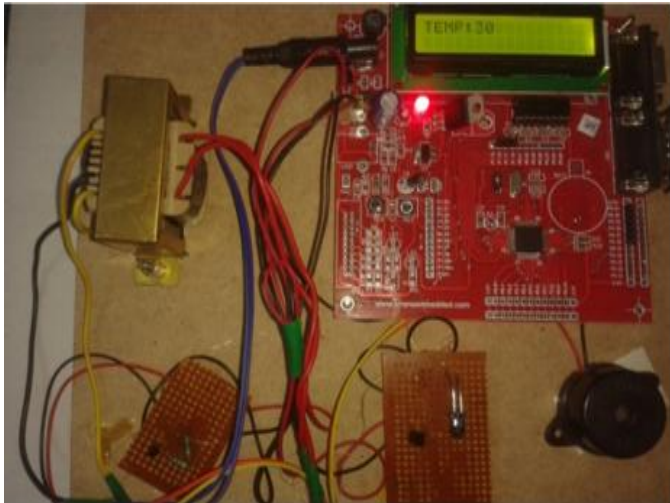


Fig. 6. Hardware

An embedded system based health monitoring and control systems for scale industries are designed. The programming module is implemented by using ARM. The system mainly consists of monitoring and control unit. Here PIC microcontroller interfaced sensors are used to measure the Patient upnormalites. This is connected to zigbee through RS - 232 cable. Then master node zigbee recived the data and display the output in LCD. In this master mode priority scheduling process is performed.

V.CONCLUSION

Real time Patient monitoring is becoming increasingly popular and powerful in Real world. This will allow the use of multiple sensors to monitor a variety of chronic and acute conditions simultaneously. In the current Situation, we have analysed a huge amount of messages are transmitted between layers. Data Acquisition node is used to collect the data from various nodes and sends data to the master node through wireless HART protocol. This is also to minimize the data collision. Based on the priority given to the various parameters the master node will perform the task. Many nodes can be implemented to monitor and control the operations from the master node. The main aadvantage of this Wireless HART is the stack is implemented on a lowest cost two processor platform. In this method sensors are controlled by manual switches. Sensor values will be displayed in the display unit depending on the switching condition.

REFERENCES

- [1] M.Ragulkumar, S.Dhivya" A Grid Vehicular Node Localization System Vanet With Liner Error Propagation" International Journal On Applications In Engineering And Technology, Vol 1, Issue 5, May 2015.
- [2] P. M. Butala, Y. Zhang, R. C. Wagenaar, and T. D. C. Little, "Wireless system for monitoring and real-time classification of functional activity," in *Proc. 2nd Workshop on NetHealth 2012, IEEE Comsnets 2012*, Bangalore, India, Jan. 2012, pp. 1-5.

- [3] B.Aswini , Mr.S.Mohammed Nizar, "A Review On Error Correction Code," International Journal on Applications in Information and Communication Engineering" Volume 2, Issue 11, November,2016.
- [4] A. Cataldo, G. Cannazza, N. Giaquinto, A. Trotta, and G. Andria, "Development of a remote system for real-time control of intravenous drip infusions," in *Proc. IEEE Int. Symp. MeMeA*, Bari, Italy, 2011, pp. 234–237
- [5] P.Matheswaran, "An Ongrid Efficient Energy System Security Vanet With Adhoc Networks," *International Journal On Applications In Engineering And Technology*, Vol. 2, Issue. 3, 2016.
- [6] Jianping Song; Song Han; Mok, A.K.; Deji Chen; Lucas, M.; Nixon, M.. A RTOS Based Reconfigurable Architecture for IWSN Stack with Arm Cortex and EM250RF Processor Support. *International Journal of Science and Research*, 2014.
- [7] Yi Yang, Ping Xiang, Mike Mantor, Huiyang Zhou. CPU-assisted GPGPU on fused CPU-GPU architectures, *High Performance Computer Architecture (HPCA)*, 2012 IEEE 18th International Symposium, 25-29 Feb. 2012.
- [8] Liu Zhongyuan, Cui Lili, Ding Hong, published a paper titled "Design of Monitors Based on ARM7 and Micro C/OS-II" 2010 IEEE.
- [9] Utz Roedig, Sarah Rutledge, James Brown, Andrew Scott, "Towards Multiprocessor Sensor Nodes". *ACM Workshop on Hot Topics in Embedded Networked Sensors (HotEmNets '10)*, 2010.
- [10] Vehbi C. Gungor; Gerhard P. Hancke. *Industrial Wireless Sensor Networks: Challenges, Design Principles, and Technical Approaches*. *IEEE Transaction on Industrial Electronics*, 2009, 4258-4265
- [11] L. Contreras y W. Granados, "Diseño de un dispositivo para la movilidad de personas con discapacidad motriz usando el método función de calidad", *Ingeniería.*, vol. 19 (1), pp. 65-82.